

ES3A THRU ES3J

SURFACE MOUNT SUPER FAST RECTIFIER

Reverse Voltage - 50 to 600 Volts Forward Current - 3.0 Ampere

FEATURES

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- For surface mounted applications
- Super fast switching for high efficiency
- Low reverse leakage
- Built-in strain relief, ideal for automated placement
- High forward surge current capability
- High temperature soldering guaranteed: 250°C/10 seconds at terminals



Case: JEDEC DO-214AB molded plastic body

Terminals: Solder plated, solderable per MIL-STD-750,

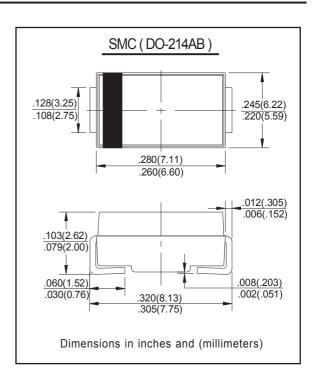
Method 2026

Polarity: Color band denotes cathode end

Mounting Position: Any

Weight: 0.007 ounce, 0.24grams





MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Characteristic	Symbol	ES3A	ES3B	ES3C	ES3D	ES3E	ES3G	ES3J	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	50	100	150	200	300	400	600	V
RMS Reverse Voltage	VR(RMS)	35	70	105	140	210	280	420	V
Average Rectified Output Current @T _C = 100°C	lo	3.0							А
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	100							А
Forward Voltage @I _F = 3.0A	VFM	0.95 1.3 1.7					V		
Peak Reverse Current $@T_A = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_A = 125^{\circ}C$	IRM	10 500							μА
Reverse Recovery Time (Note 1)	trr	35							nS
Typical Junction Capacitance (Note 2)	Cj	45							pF
Operating and Storage Temperature Range	Тј, Тѕтс	-55 to +150							°C

Note: 1. Measured with IF = 0.5A, IR = 1.0A, IRR = 0.25A.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.



Notes:

1. Rise Time = 7.0ns max. Input Impedance = $1.0M\Omega$, 22pF.

2. Rise Time = 10ns max. Input Impedance = 50Ω .

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RATINGS AND CHARACTERISTIC CURVES

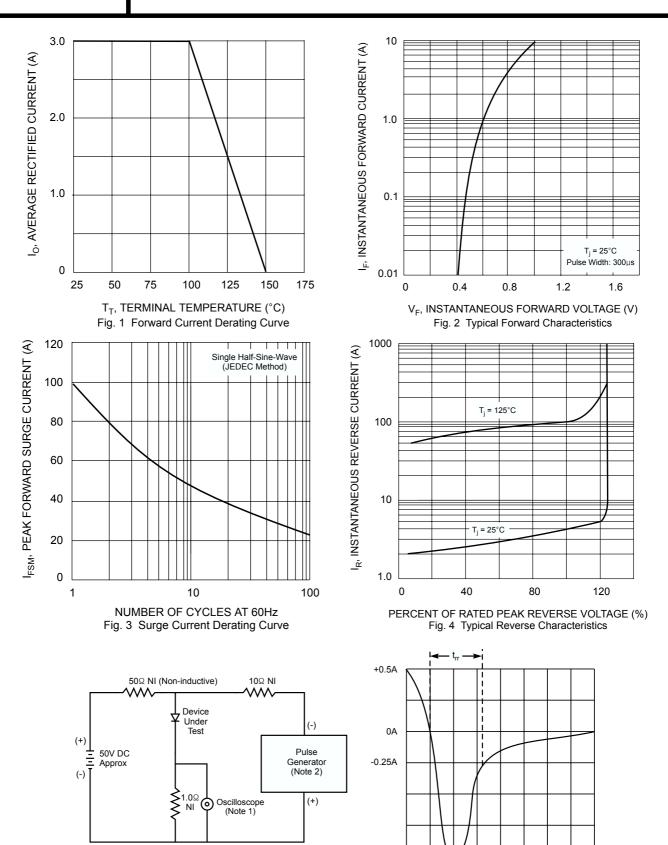


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

-1.0A

Set time base for 50/100 ns/cm